L Number	Hits	Search Text	DB	Time stamp
1	16	"neutral instruction"	USPAT; US-PGPUB	2004/07/15 16:07
-	2	pipeline with stall with (neutral NOP instruction) with test$3	USPAT; US-PGPUB	2004/02/20 13:29
-	12	stall with (neutral NOP instruction) with test$3	USPAT; US-PGPUB	2004/02/18 09:52
-	1	pipeline with stall with (neutral)	USPAT; US-PGPUB	2004/02/18 09:55
-	32	smith-ronal$.in. and g06f$.ipc.	USPAT; US-PGPUB	2004/02/18 09:54
-	42	pipeline with stall with instruction with (test$3 verif$7 check$3 analy$4 examin$3)	USPAT; US-PGPUB	2004/02/18 10:02
-	1	pipeline with stall with (insert$3 generat$3 execut$3) with NOP with (test$3 verif$7 check$3 analy$4 examin$3)	USPAT; US-PGPUB	2004/02/18 10:03
-	1	pipeline with stall with NOP with (test$3 verif$7 check$3 analy$4 examin$3)	USPAT; US-PGPUB	2004/02/18 10:04
-	426	712/227.ccls.	USPAT; US-PGPUB	2004/02/18 10:14
-	1	test$3 near3 processor with NOP	USPAT; US-PGPUB	2004/02/18 10:15
-	247	test$3 near3 processor with execut$3 with instruction	USPAT; US-PGPUB	2004/02/18 10:16
-	1	test$3 near3 processor with execut$3 with instruction same stall	USPAT; US-PGPUB	2004/02/18 10:35
-	13	"neutral instruction"	USPAT; US-PGPUB	2004/02/18 10:47
-	1	(pseudo adj random adj generator) with NOP	USPAT; US-PGPUB	2004/02/18 10:50
-	77	architectural near2 (comparison verification)	USPAT; US-PGPUB	2004/02/18 11:02
-	1	test$3 with resource with (idle near3 (processor pipeline))	USPAT; US-PGPUB	2004/02/19 08:53
-	1	(test$3 near3 register) with pipeline with stall	USPAT; US-PGPUB	2004/02/18 13:13
-	25	(test$3 near3 register) with stall	USPAT; US-PGPUB	2004/02/18 13:14
-	0	(test$3 near3 (resource hardware)) with stall	USPAT; US-PGPUB	2004/02/18 13:14
-	0	((verif$4 confirm$3) near3 (resource hardware)) with stall	USPAT; US-PGPUB	2004/02/18 13:15
-	11	("5331571" | "5737516" | "5828824" | "5970241" | "6032268" | "6055649" | "6065106" | "6081885" | "6085336" | "6112298" | "6141636").PN.	USPAT	2004/02/19 08:50
-	2	(test$3 near3 register) with ((idle stall$3) near3 (processor pipeline))	USPAT; US-PGPUB	2004/02/19 08:56
-	7	((error examin$3 check$3 verif$4) near3 register) with ((idle stall$3) near3 (processor pipeline))	USPAT; US-PGPUB	2004/02/19 08:58
-	183	714/35.ccls.	USPAT; US-PGPUB	2004/02/19 08:58
-	5	714/35.ccls. and (stall near3 pipeline)	USPAT; US-PGPUB	2004/02/19 08:59
-	13	714/35.ccls. and idle	USPAT; US-PGPUB	2004/02/19 08:59
-	1	5329471.pn. and (access$3 test$3) with (register resource)	USPAT; US-PGPUB	2004/02/19 09:51
-	0	compiler with insert$3 with NOP with stall$3	USPAT; US-PGPUB	2004/02/19 11:39
-	30	compiler with insert$3 with NOP	USPAT; US-PGPUB	2004/02/19 11:40
-	0	compiler with NOP with schedul$3 with (stall$3 hazard conflict$3)	USPAT; US-PGPUB	2004/02/19 12:49
-	4	compiler with NOP with schedul$3	USPAT; US-PGPUB	2004/02/19 12:50
-	3	compiler with NOP with (stall$3 hazard conflict$3)	USPAT; US-PGPUB	2004/02/19 12:50

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–	32	(test$3 verif$4 check$3) with register with (value contents) with (logic$2 near2 operation)	USPAT; US-PGPUB	2004/02/19 16:00
–	0	register with (logic$2 near2 operation) with (all adj3 (one ones "1's"))	USPAT; US-PGPUB	2004/02/19 16:01
–	381	register with (logic$2 near2 operation) with "AND"	USPAT; US-PGPUB	2004/02/19 16:03
–	19	register with (logic$2 near2 operation) with "AND" with mask with ("1" one ones)	USPAT; US-PGPUB	2004/02/19 16:06
–	0	test$3 with register with (logic$2 near2 operation) with ("AND" and "OR") with mask	USPAT; US-PGPUB	2004/02/19 16:07
–	5	test$3 with register with (logic$2 near2 operation) with "AND" and "OR" with mask	USPAT; US-PGPUB	2004/02/19 16:07
–	11	(test$3 verif$4) with register with (logic$2 near2 operation) with ("AND" and "OR")	USPAT; US-PGPUB	2004/02/19 16:08
–	13	register with (logic$2 near2 operation) with ("AND" and "OR") with mask	USPAT; US-PGPUB	2004/02/19 16:11
–	6	test$3 with register with ("AND" and "OR") with mask	USPAT; US-PGPUB	2004/02/19 16:32
–	337	test$3 with register with mask	USPAT; US-PGPUB	2004/02/19 16:35
–	0	test$3 with register with mask with all with (ones one "1's")	USPAT; US-PGPUB	2004/02/19 16:36
–	88	test$3 with register with mask with (ones one "1's")	USPAT; US-PGPUB	2004/02/19 16:37
–	6	test$3 with register with mask with ("AND" and "OR")	USPAT; US-PGPUB	2004/02/20 08:51
–	2	register with mask with ("AND" and "OR") with ("FF" "00")	USPAT; US-PGPUB	2004/02/20 08:52
–	119	register with mask with ("AND" and "OR") with (ones one "1")	USPAT; US-PGPUB	2004/02/20 08:57
–	20	mask with "AND" with ("11111111" "FF")	USPAT; US-PGPUB	2004/02/20 09:01
–	3545	mask with "AND"	USPAT; US-PGPUB	2004/02/20 09:01
–	97	mask with "AND" with test$3	USPAT; US-PGPUB	2004/02/20 09:01
–	29	"AND" near3 NOP	USPAT; US-PGPUB	2004/02/20 09:45
–	102	"AND" near3 neutral	USPAT; US-PGPUB	2004/02/20 09:45
–	3	logic$2 with "AND" near3 neutral	USPAT; US-PGPUB	2004/02/20 09:45
–	6	(logic$2 with "AND" with NOP) not ("AND" near3 NOP)	USPAT; US-PGPUB	2004/02/20 09:47
–	63	("AND" with NOP) not ("AND" near3 NOP)	USPAT; US-PGPUB	2004/02/20 09:48
–	44	mask with NOP	USPAT; US-PGPUB	2004/02/20 09:48
–	9	pipeline with stall with (neutral NOP instruction)	EPO	2004/02/20 13:35
–	0	test$3 with (resource hardware register) with (stall idle)	EPO	2004/02/20 13:32
–	0	test$3 with (resource hardware register) with (stall$3 idle)	EPO	2004/02/20 13:32
–	10	test$3 with (resource hardware register) with (stall$3 idle)	JPO	2004/02/20 13:34
–	6	test$3 with (resource hardware register) with (stall$3 idle)	DERWENT	2004/02/20 13:34
–	0	test$3 with (resource hardware register) with (stall$3 idle)	IBM_TDB	2004/02/20 13:34
–	0	nakajo-hisao.in. hisao-nakajo.in.	USPAT	2004/02/20 13:35
–	9	nakajo-h$.in. hisao-n$.in.	USPAT	2004/02/20 13:35

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